Special Issue

Hardware and Chip Security in Cyber Physical System

Message from the Guest Editors

The development trend of ICs results in them being extremely vulnerable to attack by hardware trojans (HTs). The prevention and control of HTs is a tough problem, and is predicted to continue to be a persistent complicated issue in the future. It is extremely urgent to study how to detect hidden HTs in ICs quickly and precisely, ensuring hardware security. For this Special Issue, we are interested in inviting researchers in the field to gather recent privacy and security methods in chips and IoTs and their applications to address these challenges and opportunities differently from traditional architectures. Topics of interest include, but are not limited to:

- Hardware security primitives;
- Applications of secure hardware;
- Hardware trojans—backdoor and detection techniques;
- IC trust and anticounterfeiting;
- Reverse engineering and hardware obfuscation;
- Security architectures in embedded systems;
- Protection of the Internet of Things (IoT);
- Security architectures for pervasive computing and wireless applications/protocols;
- Secure system-on-chip (SoC) designs;
- Lightweight cryptography and implementations;
- Cyber-physical system security.

Guest Editors

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Deadline for manuscript submissions

closed (30 April 2023)

Sensors

an Open Access Journal by MDPI

Impact Factor 3.5
CiteScore 8.2
Indexed in PubMed

mdpi.com/journal/
sensors

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Message from the Editor-in-Chief

Sensors is a leading journal devoted to fast publication of the latest achievements of technological developments and scientific research in the huge area of physical, chemical and biochemical sensors, including remote sensing and sensor networks. Both experimental and theoretical papers are published, including all aspects of sensor design, technology, proof of concept and application. Sensors organizes Special Issues devoted to specific sensing areas and applications each year.

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