Special Issue

VLSI Architectures for Wireless Communications and Digital Signal Processing

Message from the Guest Editor

The paradigm shift in wireless communication standards is gaining momentum with the increasing demand for data traffic, the number of connected nodes, and autonomously operating smart devices. This shift is reflected in the latest communication protocols, such as those on 5G, as well as multiplied connection speeds and increased connectivity support. Moreover, the same protocols require ultra-low-power operation and improved reliability, especially for the growing number of smart connected sensor devices across the globe. As a result, these connectivity demands will likely continue to grow with the upcoming, beyond-5G communications. These ever-growing demands for evolving wireless connectivity require interdisciplinary techniques in several research dimensions, many of which can be gathered under the umbrellas of advanced VLSI design and signal processing techniques.

As a result, in this Special Issue, our focus is on the state-of-the-art methods that can address the latest challenges in wireless communications by proposing concurrent solutions through signal processing techniques and demonstrating through VLSI implementations.

Guest Editor

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