## **Special Issue**

### Novel Computing Architectures and Digital Circuit Designs Using Memristors and Memristive Systems, 2nd Edition

### Message from the Guest Editors

Memristors have shown much promise as a solution for processing-in-memory architectures due to its nonvolatile memory retention, high density, low power, nanoscale geometry, and so on. With ongoing advances in material science and device physics, physically derived and empirically based memristor models have broadened the ways in which we may design, simulate, and test exotic computing systems and architectures. Furthermore, integrating memristors with modern CMOS processes technology has expanded the spectrum of research on memristive crossbar arrays, digital logic circuits, and inmemory processors, which play an important role in neuromorphic computing systems, novel computing architectures and dynamical memristive networks. The purpose of this Special Issue is to provide a comprehensive overview of memristor fabrication, characterization, and modeling; memristor crossbar arrays, memristor logic circuit designs, and processing-in-memory architectures; and other circuit or system-level applications that harness the dynamical properties of memristors.

### **Guest Editors**

Prof. Dr. Xiaoyuan Wang School of Electronics and Information, Hangzhou Dianzi University, Hangzhou 310018, China

#### Prof. Dr. Herbert Ho-Ching lu

School of Electrical, Electronic and Computer Engineering, The University of Western Australia, Perth, WA 6009, Australia

### Deadline for manuscript submissions

closed (30 December 2024)



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Micromachines Editorial Office MDPI, Grosspeteranlage 5 4052 Basel, Switzerland Tel: +41 61 683 77 34 micromachines@mdpi.com

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### Editor-in-Chief

Prof. Dr. Ai-Qun Liu

 Department of Electrical and Electronic Engineering, The Hong Kong Polytechnic University, Hong Kong, China
School of Electrical and Electronic Engineering, Nanyang Technological University, Singapore 639798, Singapore

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