

Special Issue

Network on Chip (NoC) and Reconfigurable Systems

Message from the Guest Editor

In this Special Issue, we invite academic and industrial researchers to explore the opportunities of asynchronous and GALS NoC. We encourage papers on numerous subjects of interest that include but are not limited to:

- Asynchronous and GALS NoC systems;
- GALS 2D/3D NoC topologies ;
- Asynchronous and GALS NoC routers/arbiters;
- Routing protocols in asynchronous/GALS NoC systems;
- GALS routing services for specific applications of NoC systems;
- Energy-efficient routing protocols for asynchronous and GALS NoC systems;
- Mapping algorithms for GALS NoC systems;
- Wireless GALS NoC architectures.

Papers must be personalized to subjects associated with asynchronous and GALS NoC systems. Only original, unpublished contributions and invited articles will be considered for the issue.

Guest Editor

Dr. Abdelkrim Zitouni

Department of Physics, College of Science & Humanities Jubail, Imam Abdulrahman Bin Faisal University, Dammam 31113, Saudi Arabia

Deadline for manuscript submissions

closed (20 May 2023)



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Micromachines
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
micromachines@mdpi.com

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About the Journal

Message from the Editor-in-Chief

You are invited to contribute research articles or comprehensive reviews for consideration and publication in *Micromachines* (ISSN 2072-666X). *Micromachines* is published in the open access format. Research articles, reviews and other contents are released on the internet immediately after acceptance. The scientific community and the general public have unlimited free access to the content as soon as it is published. As an open access journal, *Micromachines* is supported by the authors or their institutes by payment of article processing charges (APC) for accepted papers. We are pleased to welcome you as our authors.

Editor-in-Chief

Prof. Dr. Ai-Qun Liu

Department of Electrical and Electronic Engineering, The Hong Kong Polytechnic University, Hong Kong, China

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