

## Special Issue

# System-on-a-Chip (SoC): Design and Applications

### Message from the Guest Editors

System-on-a-Chip (SoC) is a highly integrated and heterogeneous computer system that is typically composed of analog, digital, optical, radio frequency (RF), micro-electro-mechanical systems (MEMS) etc. Today, it is widely used in communication, biomedical, and mobile applications. With the ever-increasing use of Artificial Intelligence (AI) and deep-learning-enabled applications, today's SoC finds new opportunities and challenges in architectures/platforms, systems, circuits design, design tools, and applications. This Special Issue seeks to showcase research papers, communications, and review articles on recent advances/developments in SoC focusing on design and applications. The topics of this Special Issue include but are not limited to:

- New architectures/platforms for SoC design;
- New SoC applications in diverse areas;
- New circuits for 5G/6G;
- Green circuits and systems for emerging energy/power applications;
- New design methodologies and tools for SoC;
- Design validation and verification and test for SoC;
- Network-on-Chip in SoC;
- Low-power SoC design for mobile applications.

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### Guest Editors

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### Deadline for manuscript submissions

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### Editor-in-Chief

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