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Smart Embedded Systems: A Self-Aware System-on-Chip (SoC) and Applications

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Message from the Guest Editor

Given the increasing complexity of tasks faced by embedded systems, smart cyberphysical systems are the major focus for SoC implementations, which allow the resulting embedded systems to be self-aware, adaptive, and achieve a high level of resilience in the face of highly dynamic and unpredictable environments.

The scope of this Special Issue is on smart embedded systems for novel cyberphysical applications, especially regarding embedded systems hardware. This Special Issue provides an excellent opportunity for communities from both research and industry to present new results. Of special interest are contributions that describe new methods, architectures, and applications of a self-aware system-on-chip (SoC). The topics of this Special Issue include but are not limited to cyberphysical SoC for smart embedded systems design and self-awareness for the design of architectural, physical, and circuit layers of SoC systems.



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Special Issue



Editor-in-Chief

Message from the Editor-in-Chief

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