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Low-Power Circuits for Internet-of-Things

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Message from the Guest Editors

Dear Colleagues,

Internet-of-Things (IoT) devices have become widespread in the last several years largely thanks to advances in battery life, communications technology, signal-processing algorithms, electronic fabrication, and manufacturing. In particular, innovative circuit designs in wireless/wireline transceivers, data conversion, clock generation, analog- and digital-signal processing, application-specific processors, and power management have dramatically improved the capabilities of IoT devices and lowered the adoption cost critical for massive IoT deployment. Challenges such as limited battery power, low supply voltage, noise, circuit variability, and cost continue to present bottlenecks for the next generation of IoT devices. Unique solutions through innovations in circuit topologies and architectures, hardware-accelerated signal processing, power-aware design, calibration, and bio-inspired designs are being actively investigated. This issue covers the latest developments in circuit designs for IoT applications with a specific focus on low-power techniques.

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