## Special Issue

# Nanowire Field-Effect Transistor (FET)

## Message from the Guest Editors

In the last few years, the main semiconductor industries have introduced multi-gate non-planar transistors in their core business with applications to memories and logical integrated circuits in order to achieve a larger integration on chip, increase performance, and reduce energy consumption. There is intense research underway to keep developing these devices and address their limitations in order to continue transistor scaling while further improving performance. Nanowire Field-Effect Transistors (NW-FETs) are nowadays one of the strongest contenders to replace Fin Field-Effect Transistors (FinFETs) in the following technological nodes, because of their superior electrostatic control of the channel transport via a gate-all-around gate. This Special Issue represents a good opportunity for researchers around the world to disseminate their recent progress related to NW-FETs, from three different points of view: Physics, technology and modelling. Therefore, of particular interest for this Special Issue are material properties, fabrication, design optimization, characterization, numerical and analytical modelling, and variability and circuit design.

### **Guest Editors**

Prof. Antonio García-Loureiro

Prof. Karol Kalna

Dr. Natalia Seoane

## Deadline for manuscript submissions

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Materials
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
materials@mdpi.com

mdpi.com/journal/materials





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## Message from the Editor-in-Chief

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### Editor-in-Chief

Prof. Dr. Maryam Tabrizian

 Department of Biomedical Engineering, Faculty of Medicine and Health Sciences, McGill University, Montreal, QC H3A 2B6, Canada
 Faculty of Dentistry and Oral Health Sciences, McGill University, 3640 Rue University, Montreal, QC H3A 0C7, Canada

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