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Reliability Evaluation, Simulation and Mechanical Analysis of Materials for Advanced Electronic Packaging

Guest Editor:

Prof. Dr. Chang-Chun Lee

Department of Power Mechanical Engineering, National Tsing Hua University, Hsinchu, Taiwan

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Message from the Guest Editor

To overcome the limited operational speed for nano-scaled transistors, scaling electronic devices to small and thin packaging and high-density arrangements has become the technological mainstream in designing versatile packaging architectures. Among these, a promising candidate is the advanced electronic package due to its excellent capability of heterogeneous integration. However, sequential reliability is a troublesome concern given the complex packaging structure. To address this issue, advanced concepts and approaches of packaging reliability evaluation and mechanical analysis are developed to combine chips with different sizes, process nodes, and technologies. To achieve a high operating performance, reduced cost, reliable thermal management, and mechanical stability, assessment approaches to structural design, process flow, and material characteristics with regard to related reliability problems need to be developed through experimental/numerical and theoretical work.

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Prof. Dr. Chang-Chun Lee
Guest Editor



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Prof. Dr. Maryam Tabrizian

1. Department of Biomedical Engineering, Faculty of Medicine and Health Sciences, McGill University, Montreal, QC H3A 2B6, Canada

2. Faculty of Dentistry and Oral Health Sciences, McGill University, 3640 Rue University, Montreal, QC H3A 0C7, Canada

Message from the Editor-in-Chief

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Materials Editorial Office
MDPI, St. Alban-Anlage 66
4052 Basel, Switzerland

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