Special Issue

RISC-V Architectures and Systems: Hardware and Software Perspectives

Message from the Guest Editors

This Special Issue aims to shed some light on some of the aforementioned topics, tackled by RISC-V-based initiatives and projects, by sharing the most recent and promising results with the research community. Authors are invited to submit regular papers following the *JLPEA* submission guidelines, within the remit of this Special Issue call. Topics include but are not limited to:

- RISC-V processors, micro-architecture optimizations, and hardware accelerators;
- Edge computing and ISA extension (e.g., packed-SIMD, floating point, bit-manipulation);
- System integration, atomic memory operations, OS support (e.g., hypervisor), compiler and runtimes, containerization, and virtualization;
- Simulation and emulation, performance analysis tools, and methodologies,
- Quality assurance and reliability, design verification/validation, and security (secure architectures and cryptography).

Guest Editors

Dr. Teresa Cervero

Dr. Kevin Martin

Dr. Mario Kovač

Prof. Maurizio Martina

Deadline for manuscript submissions

closed (1 October 2022)

Journal of Low Power Electronics and Applications

mdpi.com/si/118961

mdpi.com/journal/jlpea

About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

Editor-in-Chief

Dr. Davide Bertozzi
Reader in Advanced Processing Technologies, Department of Computer Science, University of Manchester, Manchester M13 9PL, UK

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