

Special Issue

RISC-V Architectures and Systems: Hardware and Software Perspectives

Message from the Guest Editors

This Special Issue aims to shed some light on some of the aforementioned topics, tackled by RISC-V-based initiatives and projects, by sharing the most recent and promising results with the research community. Authors are invited to submit regular papers following the *JLPEA* submission guidelines, within the remit of this Special Issue call. Topics include but are not limited to:

- RISC-V processors, micro-architecture optimizations, and hardware accelerators;
- Edge computing and ISA extension (e.g., packed-SIMD, floating point, bit-manipulation);
- System integration, atomic memory operations, OS support (e.g., hypervisor), compiler and runtimes, containerization, and virtualization;
- Simulation and emulation, performance analysis tools, and methodologies,
- Quality assurance and reliability, design verification/validation, and security (secure architectures and cryptography).

Guest Editors

Dr. Teresa Cervero

Dr. Kevin Martin

Dr. Mario Kovač

Prof. Maurizio Martina

Deadline for manuscript submissions

closed (1 October 2022)



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*Journal of Low Power
Electronics and Applications*
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
jlpea@mdpi.com

mdpi.com/journal/

jlpea





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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Editor-in-Chief

Dr. Davide Bertozzi

Reader in Advanced Processing Technologies, Department of Computer Science, University of Manchester, Manchester M13 9PL, UK

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 24.2 days after submission; acceptance to publication is undertaken in 3.8 days (median values for papers published in this journal in the second half of 2025).

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