

Special Issue

Low Power Design Methodologies and Applications

Message from the Guest Editors

The Journal of Low Power Electronics and Applications (JLPEA) is seeking original contributions for the forthcoming issue on low power design methodologies and applications. This issue is scheduled for publication in June, 2011. The aim of this issue is to present original design methodologies for low power design at various abstraction levels, starting with the device and technology levels, moving on through the circuit/logic level and finishing with the system and architecture levels. We intend to place special emphasis on the utilization of these methodologies in low power applications.

Guest Editors

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Deadline for manuscript submissions

closed (15 June 2011)



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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasis is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

Editor-in-Chief

Dr. Davide Bertozzi

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