

Special Issue

Low Power Design Methodologies and Applications

Message from the Guest Editors

The Journal of Low Power Electronics and Applications (JLPEA) is seeking original contributions for the forthcoming issue on low power design methodologies and applications. This issue is scheduled for publication in June, 2011. The aim of this issue is to present original design methodologies for low power design at various abstraction levels, starting with the device and technology levels, moving on through the circuit/logic level and finishing with the system and architecture levels. We intend to place special emphasis on the utilization of these methodologies in low power applications.

Guest Editors

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Deadline for manuscript submissions

closed (15 June 2011)



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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Editor-in-Chief

Dr. Davide Bertozzi

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