Special Issue

Ultra-Low-Power ICs for the Internet of Things

Message from the Guest Editor

Ultra-low-voltage/power analog and digital IC, powered by energy harvesters, faces the challenges of small area occupation, low design effort, and technology/design portability, which are needed in this Internet-of-Things (IoT) era that, in itself, has experienced exponential growth in relation to interconnected sensor nodes. In this framework, the aim of this Special Issue is to attract original research outcomes related to the design and application of ultra-low-voltage/power, digital-based and fully synthesizable ICs. The topics of this Special Issue include but are not limited to:

- Ultra-low-power interfaces for the Internet of Things: energy-efficient and power/voltage scalable, analog, mixed-signal IC;
- Inverter- and digital-based design methodologies of ultra-low power ICs;
- IC solution for ultra-low-voltage, energy and standby power consumption systems;
- Automated design methodology to fasten the time-to-market;
- Energy harvesting and power management circuit for IoT devices;
- Ultra-low-power/voltage ICs for instrumentation and communication applications.

Guest Editor

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Deadline for manuscript submissions

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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.
It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Editor-in-Chief

Dr. Davide Bertozzi
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