Special Issue

Low-Power Computation at the Edge

Message from the Guest Editors

The deployment of the Internet of Things (IoT) and the Industrial Internet of Things (IIoT) is generating new computing paradigms, as lots of small processing units are idle most of the time. As a first approach for taking advantage of all this processing potential, some of the computing required for processing data, acquired by these IoT nodes, has been moved to the nodes themselves, thus elevating so-called edge computing. In this context, it is interesting to explore the transfer of more computing tasks to the edge, emerging new distributed computing applications where the involved nodes are located at different places and interconnected by heterogeneous networks. For this type of applications, low-power processing units are required, such as hardware accelerators, hardware-implemented neural networks, or even cryptoprocessors, to guarantee security of the data being processed at the edge. Topics of interest include but are not limited to:

- Design of low-power hardware accelerators for the edge;
- Design of low-power hardware-implemented neural networks;
- Cryptographic processors for secure edge computing.

Guest Editors

Prof. Dr. Luis Parrilla Roure
Prof. Dr. Antonio García
Prof. Dr. Encarnación Castillo

Deadline for manuscript submissions

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Journal of Low Power Electronics and Applications
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
jlpea@mdpi.com

mdpi.com/journal/jlpea

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Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.
It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Editor-in-Chief

Dr. Davide Bertozzi
Reader in Advanced Processing Technologies, Department of Computer Science, University of Manchester, Manchester M13 9PL, UK

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