Special Issue

Implantable Bio-Electronic Circuits and Systems

Message from the Guest Editor

Implantable integrated circuits have long been utilized in a variety of biomedical applications, such as pacemakers and coclear implants. These devices have had a significant success and impact on human health care. Future implants will have the more ambitious goals of observing brain activity, decoding the extracted neural information, and, ultimately, restoring disabled functionality to the body. These next generation implantable ICs, which will interface with the nervous system, will be extremely helpful in better understanding neural pathways and the etiology of neurological diseases. This special issue will focus on the design, testing, and application of bio-implantable ICs. Papers on IC design with application to brain recording and stimulation are particularly encouraged.

Guest Editor

Dr. Emre Salman

Department of Electrical and Computer Engineering, Stony Brook University (SUNY), Stony Brook, NY 11794, USA

Deadline for manuscript submissions

closed (22 September 2015)



Journal of Low Power Electronics and Applications

an Open Access Journal by MDPI

Impact Factor 1.8 CiteScore 4.3



mdpi.com/si/4588

Journal of Low Power Electronics and Applications Editorial Office MDPI, Grosspeteranlage 5 4052 Basel, Switzerland Tel: +41 61 683 77 34 ilbea@mdbi.com

mdpi.com/journal/ jlpea





Journal of Low Power Electronics and Applications

an Open Access Journal by MDPI

Impact Factor 1.8 CiteScore 4.3





About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Editor-in-Chief

Dr. Davide Bertozzi

Reader in Advanced Processing Technologies, Department of Computer Science, University of Manchester, Manchester M13 9PL, UK

Author Benefits

High Visibility:

indexed within Scopus, ESCI (Web of Science), Inspec, and other databases.

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 23.4 days after submission; acceptance to publication is undertaken in 2.8 days (median values for papers published in this journal in the first half of 2025).

Journal Rank:

CiteScore - Q2 (Electrical and Electronic Engineering)