Special Issue

Hardware for Machine Learning

Message from the Guest Editors

This Special Issue focusses on hardware and circuit design methods for machine learning applications. It will include invited papers that will cover a range of topics—the large-scale integration of CMOS mixed-signal integrated circuits and nanoscale emerging devices, to enable a new generation of integrated circuits and systems that can be applied to a wide range of machine learning problems; on-device learning; in-memory computing; neuromorphic deep learning, and system-level aspects of Edge-AI. The rationale of this Special Issue is to develop a compelling volume of research in the emerging field of neuromorphic and machine learning (ML) circuits and systems, and present advances in their individual studies in this area of growing importance. We believe that this topic is timely and compelling, as there is a growing need for training ML and artificial intelligence (AI) algorithms on low-power platforms that can potentially provide an orders-of-magnitude improvement in energy-efficiency, when compared to the present focus on graphics processing units (GPUs), field-programmable gate arrays (FPGAs), and digital application-specific integrated circuits (ASICs).

Guest Editors

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Deadline for manuscript submissions

closed (1 March 2022)

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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

Editor-in-Chief

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