# Special Issue

# Low-Power Systems on Chip Enabling Internet of Things

## Message from the Guest Editor

The Internet of Things is expected to be the next-generation network connecting people to people (P2P), people to machine (P2M), and machine to machine (M2M), and will be the "network of the networks" that incorporates a diversity of functionalities and technologies in support of new applications and services in a "smart" world. Energy efficiency and miniaturization are the two most critical technical challenges for the hardware implementation of microelectronic systems enabling Internet of Things. Low-power smart systems on a chip are the key enabling solutions. This Special Issue is aimed at presenting the latest advances and future challenges in low-power system-on-chip designs and implementations for communication, sensing, processing, actuation, energy harvesting and management, enabling Internet of Things.

## Guest Editor

Prof. Dr. Domenico Zito
Department of Engineering, Aarhus University, Nordre Ringgade 1, 8000 Aarhus C, Denmark

## Deadline for manuscript submissions

closed (30 June 2015)

Journal of Low Power Electronics and Applications

an Open Access Journal by MDPI

Impact Factor 1.8
CiteScore 4.3

mdpi.com/si/3549

*Journal of Low Power Electronics and Applications*
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
jlpea@mdpi.com

mdpi.com/journal/jlpea

MDPI

# Journal of Low Power Electronics and Applications

an Open Access Journal by MDPI

Impact Factor 1.8
CiteScore 4.3

mdpi.com/journal/jlpea

MDPI

# About the Journal

## Message from the Editor-in-Chief

*Journal of Low Power Electronics and Applications* is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.
It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

## Editor-in-Chief

Dr. Davide Bertozzi
Reader in Advanced Processing Technologies, Department of Computer Science, University of Manchester, Manchester M13 9PL, UK

## Author Benefits

### High Visibility:

indexed within Scopus, ESCI (Web of Science), Inspec, and other databases.

### Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 24.2 days after submission; acceptance to publication is undertaken in 3.8 days (median values for papers published in this journal in the second half of 2025).

### Journal Rank:

CiteScore - Q2 (Electrical and Electronic Engineering)