Special Issue

Ultra-Low Power VLSI Design for Emerging Applications

Message from the Guest Editors

Power consumption is a top-most design parameter in numerous circuits and systems. It is particularly significant in nanoscale CMOS technology where scaling of power is extremely challenging. Low voltage operation has been widely explored to reduce power consumption. However, supply voltage scaling alone cannot meet the power consumption requirement of many emerging applications. Therefore, various innovative design techniques for ultra-low power consumption need to be developed. This Special Issue will present the most recent advancements in ultra-low power VLSI design for emerging applications such as Internet of Things, biomedical applications, and mobile electronics.

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Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Editor-in-Chief

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