

Special Issue

Spin-Orbit Torque/Voltage-Controlled MRAM and Low Power Application

Message from the Guest Editor

MRAM (magnetic random access memory) is the only memory technology at present that can achieve nonvolatility and is capable of an infinite number of write operations. Spin-transfer torque MRAM (STT-MRAM), which enables low power and high-speed writing, has been developed and is in the commercialization phase together with conventional MRAM. This special issue will focus on the latest developments in these fields. It will reflect a wide spectrum of research topics from experiments with its operating principle, device and circuit configuration, to neuromorphic systems and AI, IoT system application. Topics include but are not limited to:

- Physical property experiments
- Materials and device characteristics
- Stand-alone/embedded memory circuits and large scale integration
- Functional logic circuits and configuration with SOT/VC MRAM devices
- Neuromorphic circuits and systems with SOT/VC MRAM devices
- Circuits, systems, and methods for IoT and AI applications with SOT/VC MRAM

Guest Editor

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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Editor-in-Chief

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