Special Issue

Spin-Orbit Torque/Voltage-Controlled MRAM and Low Power Application

Message from the Guest Editor

MRAM (magnetic random access memory) is the only memory technology at present that can achieve nonvolatility and is capable of an infinite number of write operations. Spin-transfer torque MRAM (STT-MRAM), which enables low power and high-speed writing, has been developed and is in the commercialization phase together with conventional MRAM. This special issue will focus on the latest developments in these fields. It will reflect a wide spectrum of research topics from experiments with its operating principle, device and circuit configuration, to neuromorphic systems and AI, IoT system application. Topics include but are not limited to:

- Physical property experiments
- Materials and device characteristics
- Stand-alone/embedded memory circuits and large scale integration
- Functional logic circuits and configuration with SOT/VC MRAM devices
- Neuromorphic circuits and systems with SOT/VC MRAM devices
- Circuits, systems, and methods for IoT and AI applications with SOT/VC MRAM

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Deadline for manuscript submissions

closed (30 September 2018)

Journal of Low Power Electronics and Applications

an Open Access Journal by MDPI

Impact Factor 1.8
CiteScore 4.3

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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

Editor-in-Chief

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