Special Issue

Power Constrained Computing Systems for Next Generation IoT, HPC/HPT Architectures

Message from the Guest Editors

Next-generation computing systems (from IoT to Cloud to Exascale) are expected to embrace more sophisticated computing solutions to yield a leap in terms of performance. However, power consumption still represents the most limiting factor for chip designers. Indeed, with the end of Dennard scaling, innovative approaches must be realized to stack together ever more and more functions in a single chip. This Special Issue is focused on novel energy-aware heterogeneous computing systems. Novel architectures, algorithms, tools, and programming models to address the design of such power-constrained computing systems are welcome. Researchers, academics, and engineers are encouraged to submit original research contributions in all major areas of power-constrained computing for large-scale systems (HPC, Cloud, Edge, and IoT)

Guest Editors

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Deadline for manuscript submissions

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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

Editor-in-Chief

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