Special Issue

Design Space Exploration and Resource Management of Multi/Many-Core Systems

Message from the Guest Editors

Depending upon the application domain, multi/many-core systems demand energy efficiency, reliabilty and/or security. These demands can be fulfilled by exploring the design space by considering the software applications and the multi/many-core chips to find the design points leading to efficiency in all the required metrics. Considering varying workloads in the systems over time, efficient resource management methodologies need to be developed to meet the requirements that can perform resource management decisions online without any prior analysis or can exploit offline explore design space to take efficient run-time decisions. Topics include but are not limited to:

- Design space exploration (DSE) techniques for multi/many-core systems;
- DSE considering optimisation for one or more metrics, such as accuracy, performance, energy consumption, reliability and security;
- Resource management considering various principles, e.g., machine learning and heuristics;
- Adaptive and hieracrchical resource management;
- Approximate computing to achieve trade-offs for various metrics.

Guest Editors

Dr. Amit Kumar Singh

School of Computer Science and Electronic Engineering, University of Essex, Colchester CO4 3SQ, UK

Dr. Amlan Ganguly

Department of Computer Engineering, Rochester Institute of Technology, Rochester, NY 14623, USA

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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

Editor-in-Chief

Dr. Davide Bertozzi

Reader in Advanced Processing Technologies, Department of Computer Science, University of Manchester, Manchester M13 9PL, UK

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