

Special Issue

Selected Papers from ITC-CSCC 2019

Message from the Guest Editor

The 34th International Technical Conference on Circuits/Systems, Computers, and Communications (ITC-CSCC 2019) will be held on 23–26 June at Jeju Shinhwa World, Republic of Korea. The conference is open to all participants who are willing to broaden their professional contacts and to discuss the topics in Circuits/Systems, Computers, and Communications. This Special Issue of the *Journal of Low Power Electronics and Applications (JLPEA)* is a collaboration between the conference and JLPEA to exploit current advances in low power electronics. Low power devices and systems play an important role in modern technology, such as internet-of-things (IoT), wearable devices, low-power high performance computing, and biomedical electronics. Selected papers presented at the conference will be invited for publication in the Special Issue after suitable extension (at least 50%) and peer-review process. Papers focused on the following topics will be considered for publication: - Low-power memories

- Low-power, low leakage devices
- Energy harvesting and storage devices
- Analog and digital signal processing
- Low power circuit design

Guest Editor

Prof. Jinyoung Hwang

School of Electronics and Information Engineering, Korea Aerospace University, Goyang-si 10540, South Korea

Deadline for manuscript submissions

closed (15 September 2019)



Journal of Low Power Electronics and Applications

an Open Access Journal
by MDPI

Impact Factor 1.8
CiteScore 4.3



mdpi.com/si/29572

*Journal of Low Power
Electronics and Applications*
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
jlpea@mdpi.com

mdpi.com/journal/

jlpea





Journal of Low Power Electronics and Applications

an Open Access Journal
by MDPI

Impact Factor 1.8
CiteScore 4.3



[mdpi.com/journal/
jlpea](https://mdpi.com/journal/jlpea)



About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

Editor-in-Chief

Dr. Davide Bertozzi

Reader in Advanced Processing Technologies, Department of Computer Science, University of Manchester, Manchester M13 9PL, UK

Author Benefits

High Visibility:

indexed within Scopus, ESCI (Web of Science), Inspec, and other databases.

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 24.2 days after submission; acceptance to publication is undertaken in 3.8 days (median values for papers published in this journal in the second half of 2025).

Journal Rank:

CiteScore - Q2 (Electrical and Electronic Engineering)