Special Issue

Energy-Aware Neuromorphic Hardware

Message from the Guest Editors

During recent years, researchers throughout academia and industry have been advancing the theory, operation, and applications of neuromorphic computing systems. Recent interest in neuromorphic computing systems stems from its superior and rapidly advancing performance at tasks such as image recognition, learning of complex intelligent behaviors, and large-scale information retrieval problems such as intelligent web search. However, to attain the benefits of neuromorphic computing, high computational and energy-consumption demands of the underlying processing, interconnect, and memory devices on which software-based neuromorphic computing executes has become an intense focus of government, industry, and academic research. Innovative hardware implementations are sought to attain throughput goals within area, security, and energy constraints for orders of magnitude improvements via innovations across the hardware stack. This Special Issue of the JLPEA is dedicated to advances in all aspects of Energy and Secured-Aware Neuromorphic Hardware. We invite original submissions advancing device, circuits, and hardware architectures of neuromorphic computing systems.

Guest Editors

Dr. Yu Bai

Dr. Ronald F. DeMara

Prof. Dr. Lin Meng

Prof. Dr. Jiliang Zhang

Deadline for manuscript submissions

closed (20 June 2018)

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Journal of Low Power Electronics and Applications
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
jlpea@mdpi.com

mdpi.com/journal/jlpea

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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

Editor-in-Chief

Dr. Davide Bertozzi
Reader in Advanced Processing Technologies, Department of Computer Science, University of Manchester, Manchester M13 9PL, UK

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