

## Special Issue

# Energy Aware Scientific Computing in Distributed Architectures, Low Power Processors and HPC Hybrid Systems

### Message from the Guest Editor

Modern society is witnessing a sea change in ubiquitous computing, whereby people have embraced a slew of computing systems as indispensable parts of day-to-day existence. The computational, storage, and communication abilities of smartphones, for example, have undergone monumental changes in the past decade itself. With these changes, came a rapid and ongoing proliferation of edge computing systems and applications. Energy consumption is a growing problem of these computing platforms. There is a critical need for energy-aware architectures in many core systems, GPUs, hardware accelerators, and edge computing devices. This Special Issue will provide a selection of papers on energy-aware and low-power computing in system-on-chips, hardware accelerators, GPUs, embedded systems, or edge computing systems. Advances in algorithms for hardware systems design and novel architectures for low power are encouraged.

### Guest Editor

Prof. Dr. Sanghamitra Roy

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### Deadline for manuscript submissions

closed (28 February 2025)



## Journal of Low Power Electronics and Applications

an Open Access Journal  
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Impact Factor 1.8  
CiteScore 4.3



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## About the Journal

### Message from the Editor-in-Chief

*Journal of Low Power Electronics and Applications* is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

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### Editor-in-Chief

Dr. Davide Bertozzi

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