

Special Issue

Emerging Trends in Low-Power CMOS Technologies

Message from the Guest Editor

Recent trends in low-power integrated circuits (ICs) are driven by the increasing demand for energy-efficient electronic devices, especially in areas like IoT (Internet of Things), wearable technology, mobile devices, and edge computing. Among others, recent trends in low-power integrated circuits and systems include ultra-low power analog/mixed-signal design, voltage scaling techniques, energy harvesting, advanced power management, low-power communication protocols, emerging memory technologies, heterogeneous integration, etc. These trends reflect the growing importance of energy efficiency in IC design as devices become more pervasive and power constraints become more critical. General, theoretical, and application-oriented papers in the CMOS IC-related research area with a low-power design perspective are encouraged for publication in this Special Issue.

Guest Editor

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Deadline for manuscript submissions

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About the Journal

Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications is an open access journal which provides an advanced forum for rapid dissemination of innovative research and important results in all aspects of low power electronics and design.

It publishes reviews, regular research papers and short communications. Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. The full experimental details must be provided so that the results can be reproduced.

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