

## Special Issue

# Emerging Trends in Low-Power CMOS Technologies

### Message from the Guest Editor

Recent trends in low-power integrated circuits (ICs) are driven by the increasing demand for energy-efficient electronic devices, especially in areas like IoT (Internet of Things), wearable technology, mobile devices, and edge computing. Among others, recent trends in low-power integrated circuits and systems include ultra-low power analog/mixed-signal design, voltage scaling techniques, energy harvesting, advanced power management, low-power communication protocols, emerging memory technologies, heterogeneous integration, etc. These trends reflect the growing importance of energy efficiency in IC design as devices become more pervasive and power constraints become more critical. General, theoretical, and application-oriented papers in the CMOS IC-related research area with a low-power design perspective are encouraged for publication in this Special Issue.

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### Guest Editor

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### Deadline for manuscript submissions

15 October 2025



## Journal of Low Power Electronics and Applications

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## About the Journal

### Message from the Editor-in-Chief

Journal of Low Power Electronics and Applications (ISSN 2079-9268) is an open access journal which provides an advanced forum for the studies of electronics for low power applications. A special emphasize is made on ultralow power bio-medical applications. It publishes reviews, regular research papers and short communications.

Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

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### Editor-in-Chief

Dr. Davide Bertozzi

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