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Low Power Electronics and Applications

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Low-Power Computation at the Edge

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Deadline for manuscript submissions:
closed (15 December 2022)

Message from the Guest Editors

The deployment of the Internet of Things (IoT) and the Industrial Internet of Things (IIoT) is generating new computing paradigms, as lots of small processing units are idle most of the time. As a first approach for taking advantage of all this processing potential, some of the computing required for processing data, acquired by these IoT nodes, has been moved to the nodes themselves, thus elevating so-called edge computing. In this context, it is interesting to explore the transfer of more computing tasks to the edge, emerging new distributed computing applications where the involved nodes are located at different places and interconnected by heterogeneous networks. For this type of applications, low-power processing units are required, such as hardware accelerators, hardware-implemented neural networks, or even cryptoprocessors, to guarantee security of the data being processed at the edge.

Topics of interest include but are not limited to:

- Design of low-power hardware accelerators for the edge;
- Design of low-power hardware-implemented neural networks;
- Cryptographic processors for secure edge computing.

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Message from the Editor-in-Chief

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Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

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