



Low-Power Systems on Chip Enabling Internet of Things

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Deadline for manuscript
submissions:

closed (30 June 2015)

Message from the Guest Editor

Dear Colleagues,

The Internet of Things is expected to be the next-generation network connecting people to people (P2P), people to machine (P2M), and machine to machine (M2M), and will be the “network of the networks” that incorporates a diversity of functionalities and technologies in support of new applications and services in a “smart” world.

Energy efficiency and miniaturization are the two most critical technical challenges for the hardware implementation of microelectronic systems enabling Internet of Things. Low-power smart systems on a chip are the key enabling solutions.

This Special Issue is aimed at presenting the latest advances and future challenges in low-power system-on-chip designs and implementations for communication, sensing, processing, actuation, energy harvesting and management, enabling Internet of Things.

Prof. Domenico Zito
Guest Editor





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Message from the Editor-in-Chief

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Our aim is to encourage scientists to publish their experimental and theoretical results in as much detail as possible. There is no restriction on the length of the papers. Full experimental and/or methodical details must be provided.

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