Special Issue

Image Processing with Embedded Systems and FPGAs: AI Methods and IoT Applications

Message from the Guest Editors

Digital reconfigurable computing architectures for the formation, capture, processing, communication, analysis, and display of images, video, and multidimensional signals often target systems with constraints, which cannot be implemented in recent CPUs and GPUs. Thus, it is necessary to create a custom architecture capable of delivering a system that meets resources, power, and performance requirements. FPGAs are the most suitable technology since they offer reconfigurability and bit-level system specification capabilities. Topics of interest for this Special Issue include novel design and optimization methods of image processing systems based on FPGAs or SOC-FPGAs. Applications of interest include implementation of novel image and video systems with focus on prototyped and open-source implementations.

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Deadline for manuscript submissions

closed (31 January 2025)

Journal of Imaging

an Open Access Journal
by MDPI

Impact Factor 3.3
CiteScore 6.7
Indexed in PubMed

mdpi.com/journal/
jimaging

About the Journal

Message from the Editor-in-Chief

The imaging term, specific with journal, is to be considered in its broadest sense. Image processing, image understanding and computer vision are all terms related to imaging acquisition, its processing and the extraction of relevant information from the scene to obtain the underlying knowledge. All tasks related to the above items are oriented toward specific applications in a broad range of areas and topics. The *Journal of Imaging* is conceived as an efficient vehicle in the scientific community for the communication and transmission of the progress and research results in the topics covered.

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