Special Issue

Hardware Security and Trust

Message from the Guest Editor

This Special Issue seeks novel contributions to improve the current state-of-the-art literature on methodologies, tools, and results on architectures, experimental attacks, and countermeasures for embedded systems in the field of hardware security and trust. Topics of interest include but are not limited to the following: Embedded implementation of cryptographic algorithms; Physical attacks against embedded implementations and related countermeasures; Security of test infrastructures;

Hardware Trojans and detection techniques:

Hardware security primitives;

Secure processors and architectures:

Microarchitectural attacks: characterization, exploitation, protections;

Post-quantum cryptographic implementations; Lightweight cryptographic implementations; Secure implementation in constrained environments.

Guest Editor

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Deadline for manuscript submissions

closed (31 May 2024)

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Message from the Editor-in-Chief

The concept of *Information* is to disseminate scientific results achieved via experiments and theoretical results in depth. It is very important to enable researchers and practitioners to learn new technology and findings that enable development in the applied field.

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Editor-in-Chief

Prof. Dr. Willy Susilo

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