Special Issue

Computing Systems for Embedded Deep Learning

Message from the Guest Editors

This Special Issue aims to collect recent research with a focus on deploying DNNs in embedded computing systems. Potential topics include, but are not limited to:

- DNN models for embedded systems;
- Optimization of DNN models for embedded computing;
- Quantization and sparsification of DNN models;
- Implementation of DNN in embedded GPUs;
- Implementation of DNN in low-cost computing platforms;
- Reconfigurable architectures for DNN in embedded systems;
- Very low-power embedded platforms for DNNs;
- Coarse-grained reconfigurable architectures for embedded deep learning;
- Design methodologies for DNN on embedded systems;
- Design of DNN for IoT devices;
- Software tools to help design smart embedded systems;
- Applications of DNN on health, smart homes, smart cities, security, surveillance, etc.;
- Smart embedded systems for industrial IoT;
- Designing DNN for robotics.

Guest Editors

Dr. Mário Véstias

Department of Electronics, Telecommunications and Computer Engineering, Polytechnic of Lisbon, 1500-310 Lisboa, Portugal

Dr. Pedro Miguel Florindo Miguens Matutino

Electronic and Telecommunications and Computer Engineering Department (DEETC), the High Institute of Engineering of Lisbon (ISEL), Polytechnical Institute of Lisbon (IPL), 1069-035 Lisbon, Portugal

Deadline for manuscript submissions

closed (20 March 2024)

Future Internet

an Open Access Journal by MDPI

Impact Factor 3.6
CiteScore 8.3

mdpi.com/si/112114

Future Internet
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
futureinternet@mdpi.com

mdpi.com/journal/futureinternet

Future Internet

an Open Access Journal by MDPI

Impact Factor 3.6
CiteScore 8.3

mdpi.com/journal/futureinternet

MDPI

About the Journal

Message from the Editor-in-Chief

Future Internet is a fast-growing journal devoted to rapid publications of the latest results in the general areas of computer networking/communications and information systems, with a focus on the Internet of Things, big data and augmented intelligence, smart systems (in terms of technologies, architectures, and applications), network virtualization, edge/fog computing, and cybersecurity. Both theoretical and experimental papers are welcome. Every year, *Future Internet* also features Special Issues dedicated to specific topics within the journal's scope.

Editor-in-Chief

Prof. Dr. Gianluigi Ferrari
Department of Engineering and Architecture, University of Parma, Parco Area delle Scienze, 181/A, 43124 Parma, Italy

Author Benefits

Open Access:

free for readers, with article processing charges (APC) paid by authors or their institutions.

High Visibility:

indexed within Scopus, ESCI (Web of Science), Ei Compendex, dblp, Inspec, and other databases.

Journal Rank:

JCR - Q2 (Computer Science, Information Systems) / CiteScore - Q1 (Computer Networks and Communications)