

Special Issue

VLSI Design, Testing, and Applications

Message from the Guest Editor

The focus of this Special Issue is on the research challenges related to the design of emerging microelectronics and VLSI circuits and related systems that meet the demanding specifications of innovative applications. This Special Issue considers challenges in the fields of low power consumption, small integration areas, testing and security, without, however, being limited to them. Authors are encouraged to submit works related to emerging research topics and applications, such as hardware security, low-power IoT devices, high-performance processing cores, etc.

- device modeling
- emerging technologies
- CAD for VLSI design
- hardware/software co-design
- testing and verification
- FPGA-based design
- embedded systems
- IP cores
- low-power circuits and systems
- hardware security
- emerging applications
- VLSI for AI and ML algorithms

Guest Editor

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Deadline for manuscript submissions

closed (15 November 2022)



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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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