Special Issue

Reconfigurable Digital Systems: Development and Applications

Message from the Guest Editor

Reconfigurable digital systems are traditionally built on the basis of field-programmable gate arrays (FPGA). Currently, FPGA are part of heterogeneous computer platforms that combine different types of processing systems with new generations of programmable logic. FPGA-based systems can be specified, simulated, synthesized, and implemented with the aid of dedicated design environments.

The main aim of this Special Issue is to seek high-quality submissions that present and discuss the recent advances in the development of reconfigurable digital systems, especially focusing on application successes. The topics of interest include, but are not limited to the following:

- Design methods, tools, and compilers for reconfigurable digital systems
- Algorithms implemented on reconfigurable hardware
- Reconfigurable computing applications
- Education for reconfigurable digital systems including courses, teaching and training experience, lab equipment, design and applications

Guest Editor

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Deadline for manuscript submissions

closed (31 July 2021)

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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