

Special Issue

Novel Architectures, Tools and Applications for Reconfigurable FPGA-Based Embedded Systems

Message from the Guest Editors

Reconfigurable computing is nowadays a reality that is already being exploited in both research and industrial scenarios, which benefit from the combination of flexibility and high performance provided by this technology. Design flows for reconfigurable computing, which were previously only available in academic works, are now being integrated as part of commercial tools. However, multiple research opportunities can still be identified. In this Special Issue, we look forward to receiving original contributions providing novel architectures to exploit all of the possibilities offered by embedded reconfigurable devices. These novel architectures require custom design flows and supporting tools to extend the options already incorporated into commercial solutions. Application papers showing implementations and deployments in different scenarios are very welcome as well, with a particular accent on reconfigurable systems working under safety-critical requirements, such as aerospace or automotive. Welcome to contribute!

Guest Editors

Prof. Dr. Andres Otero

Centro de Electrónica Industrial, Universidad Politécnica de Madrid,
28006 Madrid, Spain

Prof. Dr. Eduardo de la Torre

Centro de Electrónica Industrial, Universidad Politécnica de Madrid,
28006 Madrid, Spain

Deadline for manuscript submissions

closed (15 June 2021)



Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



mdpi.com/si/55200

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)





Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di
Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus /
SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) /
CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is
provided to authors approximately 16.8 days after
submission; acceptance to publication is undertaken in 2.4
days (median values for papers published in this journal in
the first half of 2025).