# Special Issue

# **Network Slicing**

### Message from the Guest Editors

Network slicing, as the most significant enabling technology of the 5G era, allows multiple logical networks (network slices) to share the same telecommunication network infrastructure. It brings to 5G networks enhancements in flexibility, resource efficiency, and security; and thereby identifies 5G with a capability of specialization with respect to highly heterogeneous service types. We solicit original papers in areas including but not limited to:

- Architectural design of network slicing for industrial verticals;
- End-to-end network slicing;
- Energy efficiency of sliced networks;
- Machine learning and artificial intelligence for network slicing;
- Network slicing framework for the integration of terrestrial and non-terrestrial networks;
- Network slicing with heterogeneous radio access technologies;
- New business models in network slicing:
- Privacy and security in network slicing;
- Resource provisioning, orchestration, and management for network slicing;
- Service-level agreement design for sliced networks;
- Sliced network operation and management;
- Technologies of software-defined networks and network function virtualization.

This is a joint special issue with Network.

### **Guest Editors**

Dr. Bin Han

Prof. Dr. Simon Pietro Romano

Prof. Dr. Patrick Seeling

## Deadline for manuscript submissions

closed (30 November 2022)



# **Electronics**

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 5.3



mdpi.com/si/76190

Electronics MDPI, Grosspeteranlage 5 4052 Basel, Switzerland Tel: +41 61 683 77 34 electronics@mdpi.com

mdpi.com/journal/ electronics





# **Electronics**

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 5.3



## About the Journal

## Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

#### Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

### **Author Benefits**

### **High Visibility:**

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

#### Journal Rank:

JCR - Q2 (Physics, Applied) / CiteScore - Q2 (Control and Systems Engineering)

## **Rapid Publication:**

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.4 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the second half of 2024).

