

Special Issue

Nanoscale CMOS Technologies

Message from the Guest Editors

The scope of this Special Issue is to focus and report on the development of emerging techniques to overcome these challenges. Specifically, the research portfolio for this issue includes research on low-power RF, mixed-signal and analog CMOS design of circuits and systems, and power-efficient integrated circuit design for communications, sensing and biomedical applications. The topics of primary research include but are not limited to:

- Recent developments in nanoscale circuits and systems including mixed-signal circuits, RFICs and analog building blocks;
- Circuit design for emerging 2.5D or 3D IC CMOS technologies;
- Sub-threshold digital and analog circuits;
- Process and mismatch insensitive integrated circuit design;
- Signal processing and digital assisted methods for high performances circuits;
- Biologically-Inspired system and circuit design;
- Optimization methods for energy-efficient integrated circuits

Welcome to contribute!

Guest Editors

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Deadline for manuscript submissions

closed (31 January 2020)



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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).