Special Issue

Multilevel Converters

Message from the Guest Editors

The aim of this Special Issue is to publish original research regarding multilevel converters, presenting novel topologies, modulations, controls, related implementation technologies and applications, with the intention to increase efficiency, power density, reliability, robustness, to reduce cost and to comply with regulations. Refinements on existing techniques that introduce significant benefits are also welcome. Original contributions including experimental validation are expected. Topics of interest include, but are not limited to:

- Multilevel converter topologies, including multi-cell and power converter array topologies.
- Modular multilevel converter design approaches.
- Advanced multilevel modulation techniques.
- Advanced multilevel converter controls.
- Fault tolerance and reliability of multilevel converters.
- Implementation technologies, including integration, design for electromagnetic compatibility, and cooling techniques for multilevel converters.
- Applications of multilevel converters.

Welcome to contribute.

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Deadline for manuscript submissions

closed (15 December 2020)

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

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