Special Issue

Advanced Technologies in Intelligent Computer System Architecture

Message from the Guest Editors

Intelligent computer systems are increasingly gaining traction thanks to the improvements in the underlying machine intelligence theory and applications. This is also driving intelligence into embedded devices, which are expected to implement complex decision logics, with or without the support of remote networked servers, and possibly coordinate information from multiple sources to achieve a correct understanding of the environment they are operating in. Topics of interest include, but are not limited to:

- Hardware architectures and accelerators for intelligent embedded and cyberphysical systems;
- Compiler-based techniques for enforcing extrafunctional properties;
- Approximate computing and mixed-precision techniques;
- Low-power embedded systems architectures for AI and machine learning applications;
- Embedded systems compiler/runtime frameworks to support AI and machine learning applications;
- Programmability of self-driving cars and other safety-critical intelligent computer systems;
- Models and learning discretization for low-end microcontrollers;
- Performance/memory tradeoffs and optimizations;
- Hardware/software co-design and platform-based design.

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Deadline for manuscript submissions

closed (30 June 2022)

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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