Special Issue

High-Performance Computing and Its Applications

Message from the Guest Editors

High-performance computing is rapidly evolving from focusing on dedicated computational systems to a broader view of system stacks across the hardware architecture to their corresponding software system. Thus, the design and implementation of a modern. advanced high-performance computing system is now a mixture and composed problem that you have to consider on multiple components: hardware devices and their architecture; tooling and infrastructural software components such as high-performance runtime systems and device drivers; proper abstraction in the form of languages and APIs to simplify workloads for the system good; and solutions, optimizations, and strategies that build on top of former components. To this end, proper and elegant design of HPC systems remains challenging and of great importance, as it not only beneficial to HPC research itself but also to all research and industrial frontiers that utilize HPC solutions to fix their own problems. This Special Issue seeks high-quality manuscripts discussing the design, optimization of HPC systems, and their application in other cross-domain research frontiers that can utilize HPC solutions.

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Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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