Special Issue

Hardware Support for the Security of Computing Devices

Message from the Guest Editors

Ever-increasing on-chip transistor budgets give opportunities to explore hardware-supported security systems. First of all, hardware-based security mechanisms are generally more efficient than software-based ones in terms of performance and energy. Further, hardware is one system component that is typically immutable and hard to manipulate. Furthermore, hardware provides an unmatched visibility to program execution in a transparent manner, paving the way for developing novel security mechanisms. In this Special Issue, we invite original articles that deal with security issues on computing devices by taking all these advantages of hardware support for security.

- Secure processor architectures and implementations
- Hardware-based security monitoring systems
- Side-channel attacks, evaluations, and defenses
- Hardware-based trusted execution environments and security solutions
- Hardware support for secure cloud/IoT
- Cryptographic hardware design and implementation
- Simulation, testing, and verification for hardware-based security systems

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

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