

Special Issue

Reconfigurable Hardware Accelerators: Opportunities, Trends, and Challenges

Message from the Guest Editors

There is a wide range of pressing, critical research topics spanning from application-specific accelerator design, design methodologies, to programming models, abstractions, and runtime environments to make reconfigurable accelerators available and manageable. This call seeks various software/hardware solutions for reconfigurable accelerators, including traditional FPGA designs for edge and high-performance computing, as well as revolutionary technology, e.g., programmable processing in-memory architectures. Welcome to contribute.

Guest Editors

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Deadline for manuscript submissions

closed (31 January 2022)



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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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