Special Issue

Advances in Hardware-Software Codesign

Message from the Guest Editors

The aim of this Special Issue is to focus on hardware–software codesign advanced methods for signal, video, or image processing to give a large view of this complex field by covering topics that include but are not limited to the following areas:

- Embedded systems;
- Hardware architectures for signal and image processing: manycore processors, FPGAs, DSPs, GPUs, SoCs;
- Advances in algorithm-architecture mapping;
- Hardware-software synthesis, computer-aided tools (compilers, hardware synthesis);
- Reconfigurable systems;
- Real time processing;
- Vision systems;
- Smart sensors;
- Sensor data fusion:
- Systems for robotics and automation;
- Embedded artificial intelligence;
- Applications to simultaneous localization and mapping (SLAM), autonomous vehicles, UAVs, medical, biomedical, smart agriculture, smart cities.

Guest Editors

Dr. Abdelhafid El Ouardi

Dr. Sergio Rodriguez

Dr. Bastien Vincke

Deadline for manuscript submissions

closed (31 October 2023)



Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



mdpi.com/si/98317

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

mdpi.com/journal/electronics





Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) / CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

