

Special Issue

Future Networks: New Advances and Challenges

Message from the Guest Editors

The purpose of this Special Issue is to gather papers that define the advancement of future networks, and their services and challenges. We are seeking the latest original contributions that have not been published and are not currently under process in any other journal. The potential topics of interest include, but are not limited to, the following:

- Smart cities
- Network functions virtualization (NFV)
- Software defined networking (SDN)
- Internet of things
- Future network software and services
- QoS/QoE mechanisms for future networks
- Future communication architectures
- Energy harvesting and power management in future networks
- Security and privacy in future networks
- Environmental and social sustainable applications
- Edge computing, fog computing and cloud computing in future networks
- Artificial Intelligence, machine learning in future networks
- Data analytics and scalable/parallel/distributed computing algorithms in future networks

Guest Editors

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Deadline for manuscript submissions

closed (30 November 2020)



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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).