Special Issue

Modeling and Design of Power Converters

Message from the Guest Editor

Highly efficient and reliable power conversion is of paramount importance in many critical applications. This Special Issue intends to collect the latest research on power topologies and architecture leveraging wide bandgap devices and higher switching frequency magnetic cores. Potential topics include, but are not limited to:

- Application of new power conversion topologies and architectures used in high power computing, transportation electrification, and renewable energy grids.
- Advanced control and modulation methods for emerging power topologies and architecture.
- Advanced power conversion topologies and architectures enabling very high-frequency operation and high-power-density design.
- System-level optimization enabled by new power topologies and architectures. For instance, new power conversion architecture enables integrated onboard chargers for electric vehicles.
- Lifespan, reliability analysis, and modeling for new power topologies and architectures.
- EMI optimization through power topology and architecture innovation.

Guest Editor

Dr. Peng Fang Swenson College of Science and Engineering, University of Minnesota Duluth, Duluth, GA 55812, USA

Deadline for manuscript submissions

15 July 2025



Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



mdpi.com/si/184830

Electronics Editorial Office MDPI, Grosspeteranlage 5 4052 Basel, Switzerland Tel: +41 61 683 77 34 electronics@mdpi.com

mdpi.com/journal/

electronics





an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



electronics



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) / CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).