

## Special Issue

# Analog Microelectronic Circuit Design and Applications

### Message from the Guest Editor

We are inviting the wide community of practicing electronic engineers and scientists working in industry, government, and academia to participate in the Special Issue on Analog Microelectronic Circuit Design and Applications. The purpose of this Special Issue is to provide a collection of papers covering the broad spectrum of analog electronics, namely: monolithic device models, high performance analog circuits, radio-frequency communications and PLL circuits, and data converters. The goal is to provide the most up-to-date information in the field. This Special Issue's papers should stress the fundamental theories behind professional applications and demonstrate examples to reinforce this point. Extensive development of theory and details of proofs should be omitted. Topics of interest will include:

- CMOS device models
- Operational amplifiers
- Low-dropout regulators
- Sinusoidal oscillators
- Wideband amplifiers
- Analog phase-locked loop circuits
- Radio-frequency receivers
- Digital-to-analog converters

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### Guest Editor

Prof. Dr. Igor Filanovsky

Department of Electrical & Computer Engineering, University of Alberta,  
Edmonton, AB, Canada

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### Deadline for manuscript submissions

closed (31 October 2021)



## Electronics

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Impact Factor 2.6  
CiteScore 6.1



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## About the Journal

### Message from the Editor-in-Chief

*Electronics* is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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### Editor-in-Chief

Prof. Dr. Flavio Canavero

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#### High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus /  
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#### Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) /  
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#### Rapid Publication:

manuscripts are peer-reviewed and a first decision is  
provided to authors approximately 16.8 days after  
submission; acceptance to publication is undertaken in 2.4  
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the first half of 2025).