

Special Issue

Computer Architecture & Parallel and Distributed Computing

Message from the Guest Editors

As the amount of data continues to increase rapidly, conventional methods in computer architecture, system software, and parallel computing are no longer capable of ensuring efficiency. Both academia and industry are increasingly seeking more efficient design and implementation approaches. The rise of high-volume applications has posed challenges in processing data efficiently and quickly. In addition, there is an urgent demand to explore and adopt new technologies for collecting, processing, and analyzing big data effectively in this field. Furthermore, there are still many other unresolved issues in this field that require further research. In this Special Issue, leading researchers and developers from academia and industry come together to present new research on computer architectures, operating systems, storage systems, and parallel distributed computing. Submitted papers are peer-reviewed and selected based on quality and relevance to the main topic of this Special Issue.

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

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