Special Issue

Computer Architecture & Parallel and Distributed Computing

Message from the Guest Editors

As the amount of data continues to increase rapidly, conventional methods in computer architecture, system software, and parallel computing are no longer capable of ensuring efficiency. Both academia and industry are increasingly seeking more efficient design and implementation approaches. The rise of high-volume applications has posed challenges in processing data efficiently and quickly. In addition, there is an urgent demand to explore and adopt new technologies for collecting, processing, and analyzing big data effectively in this field. Furthermore, there are still many other unresolved issues in this field that require further research. In this Special Issue, leading researchers and developers from academia and industry come together to present new research on computer architectures, operating systems, storage systems, and parallel distributed computing. Submitted papers are peerreviewed and selected based on quality and relevance to the main topic of this Special Issue.

Guest Editors

Dr. Yongseok Son

School of Computer Science and Engineering, Chung-Ang University, Seoul 06974, Republic of Korea

Dr. Sunggon Kim

BigData and HPC Lab, Seoul National University of Science and Technology, Seoul 01811, Republic of Korea

Deadline for manuscript submissions

15 January 2026



Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



mdpi.com/si/181750

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

mdpi.com/journal/electronics





Electronics

an Open Access Journal by MDPI

Impact Factor 2.6 CiteScore 6.1



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus / SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) / CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

