Special Issue

Analog Circuits and Analog Computing

Message from the Guest Editor

Analog circuits inherently demonstrate immense computing parallelism, which endows analog computing with fast speed and low computational complexity. It is highly promising that analog computing in modern times will be substantially different from its past versions. The aim of this Special Issue is to attract reviews and original research outcomes related to the design of analog circuits and their applications to analog computing. The topics of interest for this Special Issue include but are not limited to:

- CMOS analog circuits for solving differential equations or linear algebraic problems;
- CMOS analog circuits for neuromorphic computing and engineering;
- Analog computing with emerging resistive memory for implementing logic gates, performing matrix operations, or emulating synapse/neuron functions;
- In-memory computing using analog physical laws, with SRAM, DRAM, or nonvolatile resistive memory devices;
- Addressing noise and accuracy issues of analog computing;
- Analog–digital hybrid architectures for high-precision analog computing.

Guest Editor

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Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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