

Special Issue

Computing-Based Network Innovations: Architecture, Theory, and Applications

Message from the Guest Editors

This Special Issue delves into the synergistic relationship between advanced computing technologies and network architectures, with a particular emphasis on 'computing power and network awareness'. It explores how innovative computing paradigms like cloud, edge, and fog computing are fundamentally transforming network design, making them more efficient, scalable, and responsive. The issue focuses on integrating computing power into networking, enhancing network awareness, and enabling more intelligent network management and optimization. It covers a range of topics, including theoretical foundations like algorithms and protocols, practical applications in sectors like IoT, smart cities, healthcare, and industrial automation, and emerging trends such as AI and machine learning in network operations, and quantum computing in network security. Furthermore, it addresses the critical aspect of sustainability in network solutions, aligning with global efforts to reduce carbon emissions.

Guest Editors

Dr. Lu Zhang

Dr. Jun Li

Dr. Yajie Li

Dr. Danyang Zheng

Deadline for manuscript submissions

closed (15 August 2024)



Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



mdpi.com/si/194519

Electronics
Editorial Office
MDPI, Grosspeteranlage 5
4052 Basel, Switzerland
Tel: +41 61 683 77 34
electronics@mdpi.com

[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)





Electronics

an Open Access Journal
by MDPI

Impact Factor 2.6
CiteScore 6.1



[mdpi.com/journal/
electronics](https://mdpi.com/journal/electronics)



About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

Prof. Dr. Flavio Canavero

Department of Electronics and Telecommunications, Politecnico di
Torino, 10129 Torino, Italy

Author Benefits

High Visibility:

indexed within Scopus, SCIE (Web of Science), CAPlus /
SciFinder, Inspec, Ei Compendex and other databases.

Journal Rank:

JCR - Q2 (Engineering, Electrical and Electronic) /
CiteScore - Q1 (Electrical and Electronic Engineering)

Rapid Publication:

manuscripts are peer-reviewed and a first decision is
provided to authors approximately 16.4 days after
submission; acceptance to publication is undertaken in 2.6
days (median values for papers published in this journal in
the second half of 2025).