Special Issue

Design of Low-Voltage and Low-Power Integrated Circuits, Volume 2

Message from the Guest Editors

This Special Issue's scope includes analogue and digital circuits focusing on improving accuracy, reliability, and signal-to-noise ratio while operating at low power. The purpose of this Special Issue is to consolidate the state-of-the-art research in low voltage and low power integrated designs. Below are the following topics to be covered:

- Novel designs of low-voltage and low-power integrated analogue and digital circuit designs;
- Analogue-to-digital converters;
- Digital-to-analogue converters;
- Low-voltage and low-power circuits for IoT applications;
- Low-voltage circuit designs for battery management systems (BMS);
- Low-voltage and low-power circuit designs for biomedical and medical applications;
- Low-power designs for sensors;
- Low-power operational amplifier circuits;
- Low-power bandgap reference circuits;
- Low-power DC-DC Converters;
- Low-power energy harvesting systems;
- Low-power control systems.

Guest Editors

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

