Special Issue

Recent Advancements of High Performance Computing (HPC)

Message from the Guest Editors

This SI aims to highlight recent advancements in HPC, focusing on innovative methodologies, tools, and applications that push the boundaries of scalability, dependability, and performance. The issue seeks to showcase cutting-edge research that contributes to the development of dependable, efficient, and adaptive HPC systems, meeting the growing demands of complex and critical applications. In this SI, original research articles and reviews are welcome. Research areas may include (but are not limited to) the following:

- Advances in parallel and distributed computing;
- Fault tolerance and recovery mechanisms;
- Al and machine learning integration in HPC;
- Performance optimization for large-scale systems;
- Dependability modeling and evaluation;
- Real-time systems and applications;
- HPC applications in smart cities, healthcare, and industrial IoT:
- Adaptive and self-healing HPC systems;
- Energy-efficient HPC architectures;
- Big data analytics and HPC.

I look forward to receiving your contributions.

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guestedited by leading experts in selected topics of interest.

Editor-in-Chief

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manuscripts are peer-reviewed and a first decision is provided to authors approximately 16.8 days after submission; acceptance to publication is undertaken in 2.4 days (median values for papers published in this journal in the first half of 2025).

