Special Issue

VLSI Architecture Design for Digital Signal Processing

Message from the Guest Editor

The main aim of this Special Issue is to seek high-quality submissions that highlight emerging applications and address recent breakthroughs in the VLSI architecture design for DSP, including design and analysis of signal processing algorithms and architecture, performance analysis of signal processing systems, VLSI design methodology, design of arithmetic circuits and VLSI components used in signal processing. The topics of interest include, but are not limited to:

- Design and implementation of signal processing systems
- Machine learning architectures for DSP
- Circuits and systems for signal processing and communications
- Cryptography architectures and hardware security
- Forward error correction architectures
- Multimedia signal processing systems
- Adaptive digital processing systems with FPGA components
- VLSI signal processing architectures
- Special purpose signal processing architectures
- SoC designs for DSP
- DSP algorithms implemented in VLSI systems
- Embedded architectures and systems
- Digital circuits and systems

Welcome to contribute!

Guest Editor

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Deadline for manuscript submissions

closed (31 May 2019)

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

Editor-in-Chief

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