

Special Issue

VLSI Implementation of Neural Networks

Message from the Guest Editors

This Special Issue on neural networks provides a forum for developing and nurturing an international community of scholars and practitioners who are interested in all aspects of neural networks and related approaches to computational intelligence. This uniquely broad range facilitates the cross-fertilization of ideas between biological and technological studies, and helps to foster the development of the interdisciplinary community that is interested in biologically-inspired computational intelligence. Accordingly, the “VLSI Implementation of Neural Networks” board represents experts in fields including psychology, neurobiology, computer science, engineering, mathematics, and physics.

The aim of this Special Issue of *Electronics* is to present state-of-the-art investigations in various VLSI Implementation of Neural Networks technologies for future applications. We invite researchers to contribute original and unique articles, as well as sophisticated review articles.

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About the Journal

Message from the Editor-in-Chief

Electronics is a multidisciplinary journal designed to appeal to a diverse audience of research scientists, practitioners, and developers in academia and industry. The journal is devoted to fast publication of latest technological breakthroughs, cutting-edge developments, and timely reviews of current and emerging technologies related to the broad field of electronics. Experimental and theoretical results are published as regular peer-reviewed articles or as articles within Special Issues guest-edited by leading experts in selected topics of interest.

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